

INSTRUCTION CACHE ASSOCIATIVE CROSSBAR SWITCH

ABSTRACT OF THE DISCLOSURE

5 A computing system as described in which
individual instructions are executable in parallel by
processing pipelines, and instructions to be executed in
parallel by different pipelines are supplied to the
pipelines simultaneously. The system includes storage for
10 storing an arbitrary number of the instructions to be
executed. The instructions to be executed are tagged with
pipeline identification tags indicative of the pipeline to
which they should be dispatched. The pipeline
identification tags are supplied to a system which controls
15 a crossbar switch, enabling the tags to be used to control
the switch and supply the appropriate instructions
simultaneously to the differing pipelines.

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